

**PATENT APPLICATION**

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<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200312982-1	10/666,174	
	APPLICANT		
	Mallikarjun Chadalapaka		
	FILING DATE	GROUP	
	Unassigned	2152	

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
/TD/	1A	6,502,203 B2	10/17/2002	Barron et al.	
	1B	6,496,940 B1	12/17/2002	Horst et al.	
	1C	6,493,343 B1	12/10/2002	Garcia et al.	
	1D	6,484,208 B1	11/19/2002	Hilland	
	1E	6,289,023	9/11/2001	Dowling et al.	
	1F	6,233,702 B1	05/15/2001	Horst et al.	
	1G	6,163,834	12/19/2000	Garcia et al.	
	1H	6,157,967	12/05/2000	Horst et al.	
	1I	6,070,253	05/30/2000	Tavallaee et al.	
	1J	6,070,198	05/30/2000	Krause et al.	
/TD/	1K	6,047,323	4/4/2000	Krause	

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
/TD/	1L	EP0757318A2	02/05/1997	Krause et al.		
	1M					
	1N					
	1O					
	1P					

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

/TD/	1Q	U.S. Patent Application Serial No. 10/400,313, filed 3-27-2003, entitled Protection Domain Groups, Inventor: Jeffrey R. Hilland et al.
/TD/	1R	U.S. Patent Application Serial No. 10/401,234, filed 3-27-2003, entitled Queue Pair/Window Association, Inventor: David J. Garcia et al.
/TD/	1S	U.S. Patent Application Serial No. 10/401,232, filed 3-27-2003, entitled Physical Mode Addressing, Inventor: Kathryn Hampton et al.

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<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200312982-1	10/666,179	
	APPLICANT		
	Mallikarjun Chadalapaka		
	FILING DATE	GROUP	
	Unassigned		

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
/TD/	2A	6,038,621	3/14/2000	Gale et al.	
	2B	6,018,620	01/25/2000	Culley et al.	
	2C	5,983,269	11/09/1999	Mattson et al.	
	2D	5,964,835	10/12/1999	Fowler et al.	
	2E	5,948,111	09/07/1999	Taylor et al.	
	2F	5,914,953	06/22/1999	Krause et al.	
	2G	5,872,941	02/16/1999	Goodrum et al.	
	2H	5,870,568	02/09/1999	Culley et al.	
↓	2I	5,822,571	10/13/1998	Goodrum et al.	
	2J	5,815,707	09/29/1998	Krause et al.	
/TD/	2K	5,809,285	09/15/1998	Hilland	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	2L					
	2M					
	2N					
	2O					
	2P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

/TD/	2Q	U. S. Patent Application Serial No. 10/401,230, filed 3-27-2003, entitled Physical Mode Windows, Inventor: David J. Garcia et al.
/TD/	2R	U.S. Patent Application Serial No. 10/401,237, filed 3-27-2003, entitled Method and Apparatus for Performing Connection Management with Multiple Stacks, Inventor: Prashant Modi et al.
/TD/	2S	U.S. Patent Application Serial No. 10/401,233, filed 3-27-2003, entitled Atomic Operations, Inventor: Dwight L. Barron et al.

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APPLICANT <b>Mallikarjun Chadalapaka</b>		FILING DATE	
GROUP			

**REFERENCE DESIGNATION                      U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
/TD/	3A	5,751,932	05/12/1998	Horst et al.	
↓	3B	5,737,604	04/07/1998	Miller et al.	
↓	3C	5,675,807	10/07/1997	Iswandhi et al.	
↓	3D	5,325,532	06/28/1994	Crosswy et al.	
/TD/	3E	5,978,858	11/02/1999	Bonola et al.	
	3F				
	3G				
	3H				
	3I				
	3J				
	3K				

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	3L					
	3M					
	3N					
	3O					
	3P					

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

/TD/	3Q	U.S. Patent Application Serial No. 10/401,236, filed 3-27-2003, entitled Signal Packet, Inventor: Dwight L. Barron
/TD/	3R	U.S. Patent Application Serial No. 10/401,231, filed 3-27-2003, entitled Shared Receive Queues, Inventor: Mallikarjun Chadalapaka et al.
/TD/	3S	U.S. Patent Application Serial No. 10/401,235, filed 3-27-2003, entitled Binding A Memory Window to A Queue Pair, Inventor: David J. Garcia et al.

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APPLICANT <b>Mallikarjun Chadalapaka</b>			
FILING DATE <b>Unassigned</b>		GROUP	

**REFERENCE DESIGNATION                      U.S. PATENT DOCUMENTS**

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	4A				
	4B				
	4C				
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	4L					
	4M					
	4N					
	4O					
	4P					

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

/TD/	4Q	U.S. Patent Application Serial No. 10/641,127, filed 08-14-2003, entitled Method And Apparatus For Accessing A Memory, Inventor: Mallikarjan Chadalapaka
/TD/	4R	U.S. Patent Application Serial No. 10/641,129, filed 08-14-2003, entitled Method And Apparatus For Implementing Work Request Lists, Inventors: Jeffrey R. Hilland et a l.
/TD/	4S	Jeffrey R. Hilland et al., "RDMA Protocol Verbs Specification (Version 1.0)" (April 2003).

EXAMINER <div style="text-align: right;">/Thomas Dailey/</div>	DATE CONSIDERED <div style="text-align: center;">05/31/2007</div>
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